

**"GOLDEN JUBILEE CELEBRATIONS 2015-2018"**

**JNTUH COLLEGE OF ENGINEERING (AUTONOMOUS), HYDERABAD  
Department of Electronics and Communication Engineering**



*A Faculty Development Program-cum-Short Term Course on*  
**"Analog IC Design (AICD) - Circuit and Layout Design  
Methodologies Using Cadence Design Flow"**

11-14 August, 2017



In collaboration with Entuple Technologies Pvt. Ltd., Bangalore

**Participation Certificate**

This is to certify that *Mr./Ms. VAI BHAV NEMA*.....of  
**DEVT. AHILYA UNIVERSITY, INDORE** has participated in a Faculty Development Program-cum-Short  
Term Course on **"Analog IC Design (AICD) - Circuit and Layout Design Methodologies  
Using Cadence Design Flow"** held during 11-14 August, 2017 in the Department of Electronics  
and Communication Engineering, JNTUH College of Engineering, Kukatpally, Hyderabad,  
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