

School of Electronics

Short Term Course on “Analog IC Design - Circuit and Layout Design Methodologies using Cadence Design Flow”

08 - 12 Jan 2019



A Short Term Course on “Analog IC Design - Circuit and Layout Design Methodologies using Cadence Design Flow” for VLSI Design was conducted in School of Electronics (University Teaching Department), Devi Ahilya University from 08 Jan to 12 Jan 2019. This five day long workshop was jointly organized with Institute of Engineering and Technology. Experts from Entuple Technologies, Bangalore gave hands on training using Cadence EDA tools. Cadence tools are important software to design electronic ICs. These tools are available in very few universities in Madhya Pradesh. The workshop was

jointly funded by Ministry of Electronics and Information Technology (Meity) and UGC XII Plan.

The short term course was inaugurated on 08 Jan 2019. Honorable Vice-Chancellor Prof. N. K. Dhakad presided the function. Dr. Sumant Katiyal, DCDC, Dr. Abhay Kumar, Head of the Department were also present. Prof. Dhakad stressed upon the changing requirement of the prevailing industry needs and called upon participants to get skilled enough so that the gap between industry and academia can be bridged. He also persuaded them to choose the path of becoming Entrepreneur and become a job giver. He emphasized on need on learning newer technologies to achieve greater heights in ones professional life.

Dr. S. Katiyal also encouraged and blessed the participants by his invigorating speech. Dr. Abhay Kumar welcomed all the participants. Dr. Manju K. Chattopadhyay from School of Electronics and Dr. Vaibhav Neema from IET coordinated the workshop. Workshop coordinator Dr. Vaibhav Neema highlighted the need of learning VLSI in current scenario. Vote of thanks was given by Dr. Manju K. Chattopadhyay.

The Short Term Course received an overwhelming response from various students and teachers of technical colleges of Indore. Total registered participants were 35. This number includes students and faculty members from Engineering colleges like SGSITS, Institute of Engineering Technology, Shri Vaishnav Institute of Technology and IPS Academy Indore. Participants involved in teaching and research on VLSI Design successfully completed the course. The training programme was participatory and hands on. Everything taught was tried out immediately on Cadence EDA tools. Participants needed to have some background in Analog and Digital Electronics, a brief idea about CMOS Circuits. The participants were acquainted with the intricacies of VLSI Design starting from Schematic to Layout Design. The workshop aimed to develop skill among technical manpower to make them Industry ready and employable. The feedbacks received from participants were very encouraging.

Event Photographs





